



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gerd Frankowsky et al. Art Unit : 2813
Serial No. : 10/630,632 Examiner : James M. Mitchelle
Filed : July 29, 2003
Title : SEMICONDUCTOR CIRCUIT MODULE AND METHOD FOR FABRICATING
SEMICONDUCTOR CIRCUIT MODULES

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This information disclosure statement is filed before grant of the patent, to be placed in the file, and not considered by the Office.

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Respectfully submitted,

Date: 1/12/06

Tonya S. Drake
Tonya S. Drake
Reg. No. 57,861

Fish & Richardson P.C.
225 Franklin Street
Boston, MA 02110
Telephone: (617) 542-5070
Facsimile: (617) 542-8906

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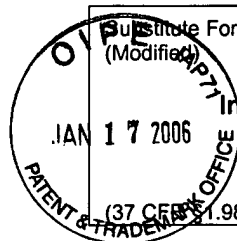
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 Attorney's Docket No.
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 Application No.
 10/630,632

**Information Disclosure Statement
 by Applicant**

(Use several sheets if necessary)

 Applicant
 Gerd Frankowsky et al.

 Filing Date
 July 29, 2003

 Group Art Unit
 2813

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	2002/0036054	3/28/2002	Nakatani et al.			
	AB	4,635,356	1/13/1987	Ohuchi et al.			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
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Foreign Patent Documents or Published Foreign Patent Applications

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							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AQ	Austrian Patent Office Search Report dates October 7, 2005, 6 pages
	AR	
	AS	
	AT	

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